



Attorney Docket No.: YOR920030640US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Luk et al.
Docket No.: YOR920030640US1
Serial No.: 10/751,713
Filed: January 5, 2004
Group: Unassigned
Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: [Signature] Date: March 12, 2004

Title: 3T1D Memory Cells Using Gated Diodes and Methods of Use Thereof

TRANSMITTAL LETTER

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Preliminary Amendment;
2. Marked-Up Figures 21 and 25; and
3. Substitute Figures 21 and 25.

There is no additional claim fee due in connection with the Preliminary Amendment.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. Duplicate copies of this letter are enclosed.

Respectfully submitted,

Date: March 12, 2004

[Signature]
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PRELIMINARY AMENDMENT

Mail Stop Non-Fee Amendment
Commissioner for Patents
20 P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

25 Please amend the above-identified patent application as indicated on the separate amendment sheets prior to substantive examination on the merits.

IN THE DRAWINGS

Please amend FIGS. 21 and 25 as indicated on the attached marked-up copies of FIGS. 21 and 25. No new matter is introduced.